



Project Fellow Recruitment in BITS Project (PLN/AD/2020-21/2)

Applications are invited from eligible and motivated candidates to work as a Project Fellow (PF) in a project funded by BITS-Pilani at Electrical and Electronics Engineering Department, BITS-Pilani, (Pilani Campus), Rajasthan. The details are as follows:

Topic of the project work	Design and implementation of protocols for autonomous UAV Networks for search and rescue application
Position Duration	Till 31 st December 2021. It will be extended based on availability of funding from BITS. It is highly likely to be extended
Principal Investigator (PI)	Dr. G S S Chalapathi
Project Description	Unmanned Aerial Vehicle (UAV) Networks are being deployed for various disaster management operations in the recent times. The project would focus on the following aspects: 1. Development and implementation of secure routing and other communication protocols for UAV networks. 2. Investigating the feasibility of the use of blockchains for UAV networks for secure data transmission 3. Deployment of UAV network for search and rescue operations.
Stipend Amount	₹31,000 per month. The stipend for this position will be paid through the OPERA Grant of the PI.
Essential Qualifications:	1. M.E./ M.Tech in Embedded Systems/ Computer Science from a reputed institute with a min. CGPA of 7.5 or 75% (percentage system) 2. Previous project experience in Machine Learning, Deep learning and Image processing is a must.
Desirable Qualification:	1. Good written and spoken English. 2. Good publication record 3. A zeal to publish in top-tier journals

Application Procedure:

1. The candidate can apply using the Google form <https://forms.gle/f4EUSvDuEXNCLasF7> **The last date for applying is 12th July 2021.**
2. Shortlisted candidate will be interviewed through Skype or similar online platform. Notification of the same will be sent to the shortlisted candidate through email.
3. The candidates selected through the interview need to join immediately by reporting to BITS-Pilani, Pilani Campus.
4. **Students with M.Tech. in Power Electronics/ Electrical Engineering/ Power Systems and related branches in Electrical Engineering NEED NOT APPLY TO THIS POST**

Notes:

1. For any queries regarding the position, please email the PI.
2. The position mentioned is temporary and is for the period mentioned. If the performance of the candidate is found unsatisfactory, the position can be terminated with a 1-month notice.
3. No TA/DA will be paid for attending the interview.

Dr. G S S Chalapathi (PI)

