



CALL FOR RESEARCH STAFF (JRF) POSITION

Date: 12 March 2024

About: Applications are invited from interested and motivated candidates for the post of Junior Research Fellow (JRF) in a time bound research project sponsored by the Science & Engineering Research Board (SERB, F. No. CRG/2023/008040) for a temporary period purely on contractual basis as per the following details:	
Position:	Junior Research Fellow (JRF)
Number of Vacancy:	01
Project Title:	Reconfigurable Holographic Surfaces for Wireless Communication: Physical Layer Algorithms and Protocol Design
Funding Agency:	SCIENCE & ENGINEERING RESEARCH BOARD (SERB)
Principal Investigator (PI):	Dr. Syed Mohammad Zafaruddin, Department of Electrical and Electronics Engineering
Project Tenure:	36 Months
Job Description:	The JRF will undertake theoretical and simulation analysis for reconfigurable holographic surfaces (RHS) based wireless communication networks. Additionally, the JRF will be responsible for algorithm and protocol development for next-generation wireless systems utilizing RHS transmission. The JRF will further conduct experimental setups to characterize RHS-based wireless systems and compare results with established theoretical models. The JRF will draft research papers for publication in indexed journals and conferences and apply for patents.
Qualification:	M.E./M.Tech in Communication Engg./ Electronics and Communications Engg /Wireless Communications/Digital Communications/Digital Signal Processing Or BE/B.Tech in Electronics and Communications Engg /Electrical/Electronics Engg Desirable: GATE/NET qualified
Upper Age Limit:	Age not exceeding 32 years as of the date of the interview. Age relaxation is allowed up to 5 years for SC/ST/PH/OBC/WOMEN candidates.
Fellowship:	37,000/- (initially first two years) and 42,000/- (afterward till the end of the project) per month for GATE/NET-qualified candidates . 25,000/-(initially first two years) and Rs 28,000 (afterward till the end of the project) per month for non-NET/GATE qualified candidates .
Registration for PhD.:	Selected candidates may be permitted to register for the PhD program of BITS Pilani subject to fulfilling the requirements of the PhD qualification process as per the institute norms.

How to Apply:

Applications along with updated CV should be sent through mail to syed.zafaruddin@pilani.bits-pilani.ac.in Shortlisted candidates will be informed of the interview which will be conducted through online mode. Mere possession of minimum qualification does not guarantee an invitation to the interview. Candidates will be shortlisted based on their merit and as per the requirements of the project. Please note that only qualified and suitable candidates will be called for an interview.

Application Deadline: 25 March 2024

Contact Email: syed.zafaruddin@pilani.bits-pilani.ac.in

